TIMED PETRI NETS FOR SOFTWARE APPLICATIONS

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Abstract. This paper presents a model of interpreted timed Petri nets in an abstract program environment called Virtual Decision System (VDS). There are described possibilities of software implementation with technical limitations of mentioned method. It shows a practical example with elements of deviation analysis. Possibilities of interpretation of dynamic parameters with special taking automatic decomposition algorithm into Hardware/Software Co-Design systems are described.

Key Words. Timed Petri Nets, Digital Microsystems, Software Applications

1. INTRODUCTION

There exist a lot of ways for formal specification of digital control systems. The most of all (at present) are various modifications of Finite State Machine (e.g. CFSM, HCFSM), flow graphs (e.g. DFG, CDFG), hardware description languages (e.g. VHDL, Verilog) and Petri nets [7]. The latter have a special importance in designing process for the sake of rich formal verification apparatus and naturalness of concurrency aiding [5].

In designing process of control system very often a situation occurs in which specific aftereffects of processes are strongly dependent on time. There is a simple example showing this problem on a simplified control system of initial washing in automatic washer (Fig.1).

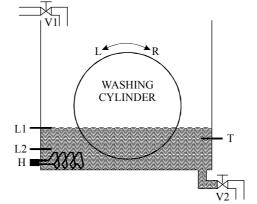


Fig. 1.An example of automatic washer control system

After turning on washing program valve V1 is opened and water is infused. Infusing process lasts to moment achieving of L1 level. In the same time after exceeding L2 level (total sinking of heater H) if the temperature is below required (TL1) heater system is turned on. After valve

V1 closing and water heating to temperature TL2 washing process is started in which the washing cylinder is turned alternate left and right for 10 sec. with 5 sec. break between. Keeping of temperature process is active for whole the washing cycle. The cycle is turned off after 280 sec. and cylinder is stopped, the heater is turned-off and the valve V2 is opened for water removing.

So, this problem is possible to describe by a hardware description language using *wait* and *after* instructions. But synthesis of them can give a lot of problems because these language constructions are not supported by synthesis tools. The other formal specification models (except Petri nets) don't allow a timing dependence in general.

In this article a new model of interpreted timed Petri nets for software applications is proposed, with its synthesis method. The method allows easy and cheap a practical implementation for shown dynamic system.

2. VIRTUAL DECISION SYSTEM

Program implementation of Petri net is possible in various ways. One is defining an abstract program environment making possible efficient implementation of control systems.

The proposed program environment is defined in terms of net places, names of input/output signals and program decision system called the Virtual Decision System VDS (Fig.2).

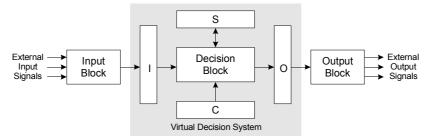


Fig. 2. Virtual Decision System

Where S – is a block storing information about system state, C – is a specification block, I – is a block storing the names of active input signals: I = { $x \in X : x = 1$ }, O – is a block storing the names of active output signals: O = { $y \in Y : y = 1$ }. It was broadly described in [8].

3. PROGRAM INTERPRETED PETRI NET

In [4] were introduced definitions describing program Petri net with most important kind of its: interpreted net, net with enabling and prohibit arcs. There is given a definition of program interpreted Petri net as a basis for further theoretical studies.

Program interpreted Petri net is a 5-tuple:

$$IPPN = (P, C, S_0, X, Y)$$

$$(1)$$

)

where: P is a non-empty set of names of net places, S_0 – is an initial net state, X is an input alphabet, Y is an output alphabet and C – is a specification structure composed of tables C¹ and C², such that: C¹ has n×3 size (n describes a number of decision rules) and C¹_{i1} i C¹_{i2} fields include sets of names of net places, which are correspondingly the conditions and the results of decision rule, C¹_{i3} field includes conditions in logical formulas form b(X) described on input alphabet; C² has m×1 size (m = ||P||) and each of fields includes set of names of output signals assigned to given place.

The conditions of rule *i* enabling:

$$\forall p \in C^{1}_{i1} : p \in S$$

$$b \in C^{1}_{i3} \Rightarrow b(X) = 1$$

$$(2-a)$$

$$(2-b)$$

The actions associated with rule *i* firing:

$$\forall \mathbf{p} \in \mathbf{C}^{1}_{i1} : \mathbf{S} = \mathbf{S} - \mathbf{p} \tag{3-a}$$

$$\forall \mathbf{p} \in \mathbf{C}^{1}_{i2} : \mathbf{S} = \mathbf{S} + \mathbf{p} \tag{3-b}$$

$$\forall (\mathbf{p} \in \mathbf{C}^{1}_{i1}) \forall (\mathbf{y} \in \mathbf{C}^{2}_{p}) : \mathbf{O} = \mathbf{O} - \mathbf{y}$$
(3-c)

$$\forall (\mathbf{p} \in \mathbf{C}^{1}_{i2}) \forall (\mathbf{y} \in \mathbf{C}^{2}_{p}) : \mathbf{O} = \mathbf{O} + \mathbf{y}$$
(3-d)

That means that from block S the names of places belonging to C_{i1}^1 are removed and belonging to C_{i2}^1 are inserted. As well as from block O the names of output signals included in C^2 table's fields corresponding with places belonging to C_{i1}^1 are removed and the names of output signals included in C^2 table's fields corresponding with places belonging to C_{i1}^1 are removed and the names of output signals included in C^2 table's fields corresponding with places belonging to C_{i1}^1 are removed and the names of output signals included in C^2 table's fields corresponding with places belonging to C_{i2}^1 are inserted.

4. TIMED PETRI NETS

Timed Petri nets were introduced earlier in literature [1,6]. And yet the subject matter wasn't developed for the sake of implementation difficulties mainly. A new program model of timed Petri net is proposed in this paper, that allows an efficient implementation in microprocessor modules.

A general program model of interpreted timed Petri net is shown as an ordered 6-tuple:

$$TIPPN = (P, C, S_0, X, Y, T)$$

$$(4)$$

where: P, S₀, X, Y are defined just like (1), C is a specification block defined according to a kind of described net, and T is discrete scale of time. The differences will be stressed also in conditions of rules enabling and actions connected with their firing.

There is modified block S, keeping information about system state. For timed nets it can be a structure composed of following elements:

 $S_1 = \{p: p \in P\}$ – includes names of places currently marked, being outside a keeping state,

 $S_2^1 = \{p: p \in P\}$ – includes names of places currently marked, being inside a keeping state,

 $S_2^2 = \{t: t \in T\}$ – includes numbers assigned to discrete scale of time and describing state of timing actions in accordance with the places in S_2^1 ,

 $S_{3}^{1} = \{p: p \in P\}$ – includes names of places prepared for marking after closing adequate timing actions assigned to given rules firing,

 $S_3^2 = \{t: t \in T\}$ – includes numbers assigned to discrete scale of time and describing state of timing actions in accordance with the places in S_3^1 .

4.1. Program timed net of P-type

The time parameters are assigned to places in the nets of P-type and they are called keeping times. The keeping times can be perceived as times of staying marker in place p what is shown in Fig.3-a) (along with practical interpretation).

The condition of transition T2 enabling is a time (2 sec.) passing since moment of introducing marker into place P1. In practice this situation is interpreted as starting external timing action by signal c1. The end of action is indicated by set signal c2 conditioning transition T2.

A theoretical model is consistent with (4). Specification block C has differences to (1):

table C² has m×2 size, into C²₁(p) are included names of output signals assigned to place p, and into C²₂(p) are included numbers assigned to discrete scale of time T, describing the keeping time of marker in place p.

The condition of rule *i* enabling (just like 2-b) and:

 $\forall p \in C_{i1}^{1} : p \in S_1$

The actions associated with rule *i* firing:

(5)

$$\forall \mathbf{p} \in \mathbf{C}^{1}_{i1} : \mathbf{S}_{1} = \mathbf{S}_{1} - \mathbf{p} \tag{6-a}$$

$$\forall \mathbf{p} \in \mathbf{C}^{1}_{i2} : \mathbf{C}^{2}_{2}(\mathbf{p}) = \mathbf{0} \Longrightarrow \mathbf{S}_{1} = \mathbf{S}_{1} + \mathbf{p}$$
(6-b)

$$\forall p \in C^{1}_{i2} : C^{2}_{2}(p) \neq 0 \Longrightarrow S^{1}_{2} = S^{1}_{2} + p \text{ i } S^{2}_{2} = S^{2}_{2} + C^{2}_{2}(p)$$
(6-c)

That means that from block S_1 the names of places belonging to C_{i1}^1 are removed and belonging to C_{i2}^1 are inserted to S_1 if their keeping time equals zero ($C_2^2(p) = 0$), or to S_2^1 if not. Simultaneously a variable of timing action is added to S_2^2 (with initialization by $C_2^2(p)$ value).

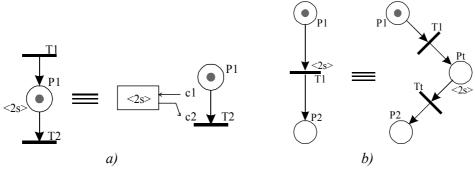


Fig. 3.An example of a) P-type net, b) T-type net

4.2. Program timed net of T-type

The time parameters are assigned to transitions in the nets of T-type and they are called execution times. The execution times can be perceived as times since moment of removing markers from input places to moment of inserting marker to output places given transition *t*. The situation can be interpreted with ideas P-type net introduced in 4.1. During transition T1 execution (Fig.3-b)) marker is moved to auxiliary place Pt. End of this execution (equivalent to keeping time of place Pt) allows auxiliary transition Tt execution. In consequence the marker is moved to output place P2.

A theoretical model is consistent with (4). Specification block C has differences to (1): table C¹ has n×4 size, fields C¹_{i1}, C¹_{i2}, and C¹_{i3} just like (1), and into C¹_{i4} are included numbers assigned to discrete scale of time T, describing execution time of transition *t*.

The condition of rule *i* enabling just like (5). The actions associated with rule *i* firing just like (6-a) and:

$$C^{1}_{i4} = 0 \Rightarrow \forall p \in C^{1}_{i2} : S_{1} = S_{1} + p$$

$$C^{1}_{i4} \neq 0 \Rightarrow \forall p \in C^{1}_{i2} : S^{1}_{3} = S^{1}_{3} + p \text{ i } S^{2}_{3} = S^{2}_{3} + C^{1}_{i4}$$
(7-a)
(7-a)
(7-b)

That means that from block
$$S_1$$
 the names of places belonging to C_{i1}^1 are removed and belonging to C_{i2}^1 are inserted to S_1 if execution time of rule equals zero ($C_{i4}^1 = 0$), or to S_3^1 if not. Simultaneously a variable of timing action is added to S_3^2 (with initialization by C_{i4}^1 value).

4.3. Program timed net of PT-type

Timed Petri net PT-type is a superposition of nets defined in 4.1 and 4.2. There are the time parameters assigned with both places and transitions. A theoretical model is consistent with (4). Specification block C is composed of two tables: C^1 (just like in 4.2) and C^2 (just like in 4.1).

The condition of rule *i* enabling just like (5). The actions associated with rule *i* firing just like (6-a) and:

$$C^{1}_{i4} \neq 0 \Rightarrow \forall p \in C^{1}_{i2} : S^{1}_{2} = S^{1}_{2} + p i S^{2}_{2} = S^{2}_{2} + C^{1}_{i4}$$
 (8-a)

$$C^{1}_{i4} = 0 \Longrightarrow \forall p \in C^{1}_{i2} : C^{2}_{2}(p) = 0 \Longrightarrow S_{1} = S_{1} + p$$
(8-b)

$$C_{i4}^{1} = 0 \Rightarrow \forall p \in C_{i2}^{1} : C_{2}^{2}(p) \neq 0 \Rightarrow S_{3}^{1} = S_{3}^{1} + p i S_{3}^{2} = S_{3}^{2} + C_{2}^{2}(p)$$
 (8-c)

That means that from block S_1 the names of places belonging to C_{i1}^1 are removed and belonging to C_{i2}^1 are inserted to S_1 if execution time of rule equals zero ($C_{i4}^1 = 0$) and keeping time equals zero ($C_{i2}^2(p) = 0$), or to S_2^1 if execution time of rule doesn't equal zero ($C_{i4}^1 \neq 0$), or to S_3^1 if execution time of rule equals zero ($C_{i4}^2 = 0$) and keeping time doesn't equal zero ($C_{i4}^2 = 0$). The variables of timing action are adding to S_2^2 and S_3^2 simultaneously with (8-a) and (8-c).

5. RESULTS AND APPLICATION

So the defined environment and program implementation model of interpreted Petri net enables simple implementation with any high level language. Specification of blocks C and S can be declared as structures composed by required number of tables; blocks I and O – as global variables. Decision block can be declared as a system of functions operating on mentioned blocks. It was broadly depicted in [2,4].

This paper presents only methodology of initializing and performing of timing actions in simple systems with industrial standard microcontrollers.

The idea of executing timing actions relies on using interrupt system in microprocessor module. In general it must generate an interrupt with given constant frequency (e.g. 1 kHz). It is possible to realize by using a generator connected to external interrupt pin or by using an internal timer/counter (more recommended). The overflow that timer is indicated by calling right interrupt. The interrupt handling procedure is used to decrement of auxiliary registers storing information about actual state of timing actions.

At the initialization moment of timing action there is granted suitable variable with initial value $t_p = t_a * f_i$, where t_a is a required time of action, f_i is a frequency of calling interrupt *int*. The value of t_p is taken from right fields of specification block C (C_{i4}^1 and $C_2^2(p)$).

During interrupt handling procedure microprocessor performs decrement operations all variables of timing actions. If any variable equals zero then it is taken operation assigned to its ending and in next step it is removed from variables list:

$$\forall (p \in S_{2}^{1}) S_{2}^{2} = 0 \Rightarrow S_{2}^{1} = S_{2}^{1} - p \text{ and } S_{2}^{2} = S_{2}^{2} - S_{2}^{2}(p) \text{ and } S_{1} = S_{1} + p$$
 (9-a)

$$\forall (p \in S^{1}_{3}) S^{2}_{3} = 0 \Longrightarrow S^{1}_{3} = S^{1}_{3} - p \text{ and } S^{2}_{3} = S^{2}_{3} - S^{2}_{3}(p) \text{ and } S_{1} = S_{1} + p \tag{9-b}$$

Contents of block O is updated according to contents of blocks S_1 oraz S_2^1 :

$$\forall [p \notin (S_1 \cup S_2^1)] \forall [y \in C_1^2(p)] O = O - y$$
(10-a)

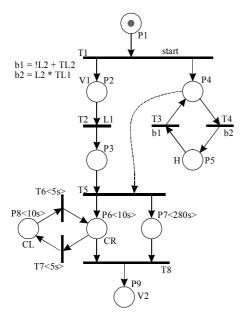
$$\forall [\mathbf{p} \in (\mathbf{S}_1 \cup \mathbf{S}_2^1)] \; \forall [\mathbf{y} \in \mathbf{C}_1^2(\mathbf{p})] \; \mathbf{O} = \mathbf{O} + \mathbf{y} \tag{10-b}$$

A precision of timing action is dependent on a lot of factors in proposed system. The most important are: inaccuracy of generating interrupt system, delay of calling interrupt procedure, time of interrupt handling, number of active timing actions, time of updating state block S, reaction time of decision block and time of function I/O handling.

Suppose sufficient precision generating interrupt system there we can omit the deviation assigned to it. The other factors are strongly dependent among other things on used processor, its clock speed and quality of executable program code generated by given compiler.

In general the deviation of timing action is linear to number of decision rules and it's contained in given range. The range is possible to calculating in analysis process. A specific value of this deviation (in the range of course) for given rules or places is dependent on location the rules in specification block C. This is a result of sequential examining block C by decision block in permitted rules searching.

A net modeling that system and an obtained results are presented in Fig.4.



Parameter	Result
Total program memory occupation	968 B
Data memory occupation	148 B
Occupation of specification block C	100 B
Deviation of timing action (min.)	44 µs
Deviation of timing action (max.)	80 µs
Reaction time (min.)	0.6 ms
Reaction time (max.)	2,2 ms

Fig. 4. Fragment of the net modelling washer controller with the results

As a test system was adopted single-chip microcontroller PCF 80C51HB-3 with 12MHz clock. Program was compiled with μ Vision 2 tool (Keil Software).

6. SUMMARY

Application of the timed model Petri nets not only can effectively help on description level for reactive system strongly time depended but it can be used in system decomposition automation too (in Hardware/Software Co-Design sense [3]).

Further works and researches are to steer on studying generalized model for complicated hierarchical nets. We are going to consider not only simplifications of implementation for nets with complicated topology but also implementation of nets in distracted systems.

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