

Registration form

2nd International Workshop on
Discrete-Event System Design, DESDes'04

**Dychów near Zielona Góra,
September 15÷17, 2004**

Last name:

First name:

Affiliation:

Address:

City:

Zip/Postal Code:

Phone:

Fax:

E-Mail:

Accompanying persons: ☐ no ☐ yes
 children: ☐ no ☐ yes

Additional night(s): ☐ no ☐ yes

Date(s):

Arrival date:

Departure Date:

Please indicate any special needs
(e.g. vegetarian food) :

.....

*Please fill in completely and send by e-mail/fax/post to the
correspondence address.*

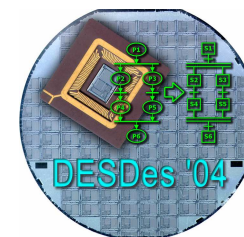
LOCATION:

The Pensjonat Dychów is located on the Zielona Góra–Gubin highway, close to the western border.

Guests can choose single or double room with air conditioning. The rooms are equipped with refrigerator, satellite TV, radio and telephone.

The hotel also boasts a covered swimming pool, a power gym, a sauna, a solarium and a fitness room. Equally one can relax with game of billiards. The Guest Center can offer an 18-hole mini-golf course, tennis court and basket ball court, the modern surface of which ensures a very comfortable and safe game. The surrounding woods give us the opportunity to organize hunts, picnics, mushroom collection and rides on horse-drawn carriage on horse back. Moreover, being located in the immediate neighborhood of reservoirs creates a healthy microclimate and attractive cycle paths and walks.

Hotel homepage (only in Polish and German:
<http://www.dychow.hotel.pl>)



CALL FOR PAPERS

2ND INTERNATIONAL WORKSHOP ON

**DISCRETE-EVENT
SYSTEM DESIGN**

DESDes'04

Organized by

University of Zielona Góra,
Institute of Computer Engineering
and Electronics



SCOPE

DESDes'04 International Workshop is the second workshop on Discrete-Event System Design organized by University of Zielona Góra, Poland. It continues experience of fruitful international co-operation in the subject with the view of extending the scope and fields of interests.

The aim of the Workshop is to bring together researchers from universities and industry and provide a platform to report and discuss new topics in digital system design and digital signal processing.

The Workshop is devoted to subjects concerning design, analysis and implementation of discrete-event systems, formal design methodology, digital signal processing and related topics.

TOPICS

- LOGIC CONTROLLER DESIGN;
- PROGRAMMABLE LOGIC;
- HARDWARE DESCRIPTION LANGUAGES (HDLs);
- PETRI NET-BASED DIGITAL DESIGN;
- HARDWARE-SOFTWARE CO-DESIGN;
- SAFETY CRITICAL COMPUTER CONTROL APPLICATIONS;
- REAL-TIME SYSTEMS ISSUES;
- DIGITAL SIGNAL PROCESSING;
- DIGITAL IMAGE PROCESSING AND VISUALIZATION;
- MICROPROCESSOR SYSTEMS.

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ORGANIZING COMMITTEE

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IMPORTANT DATES

Abstract submissions due:	15 February 2004
Preliminary acceptance:	01 March 2004
Final papers due:	15 May 2004
Notification of acceptance:	15 June 2004
Early Registration:	15 July 2004
Late Registration:	after 15 July 2004
Workshop:	15-17 September 2004

SUBMISSIONS

We invite authors to submit abstract of about 250 words. Preliminary acceptance will be sent to the authors by 01 March 2004. Full papers of up to 6 pages in PDF format should be send by e-mail to:

desdes04@iie.uz.zgora.pl

by the deadline indicated above.

The authors are asked to submit, with their papers, the following information:

- Paper Title
- Author's Name, Contact Address & E-mail
- Abstract
- Keyword list

FINAL PAPERS

The final papers must be prepared according to the Author's Kit instructions as required for IFAC meetings. A corresponding PDF, Word, LaTeX2e style file can be downloaded from our WWW server:

www.iie.uz.zgora.pl/desdes04.

Accepted papers must be presented in person at the Workshop.

WORKSHOP FEES

	early	late
Registration Fee	350€	400 €
Reduced Author's Fee	250€	300 €

ALL INCLUSIVE

Fee includes conference proceedings, accommodation in Dychów hotel (in single or double rooms), full board (all meals), banquet, and coffee breaks.

Reduced fee for young scientists is available (on request).

Additional accommodation in hotel could be reserved in advance. The price is about 50€ per night.